	PATENT NUMBER	
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U.S. UTILITY Patent Application

PATENT DATE

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/903601	F	382	145	2623	J. Marcaso

"Ryoichi, Matsuoka

Virginia Kibler

Semiconductor wafer pattern shape evaluation method and device BEST AVA LABLE COPY

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TERMINAL	\$	DRAWINGS		CLAIMS ALLOWED					
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